

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	transistor with paralle with series	US-PGPUB; USPAT	OR	ON	2007/01/03 15:13
L2	12027	transistor with parallel with series	US-PGPUB; USPAT	OR	ON	2007/01/03 15:14
L3	6202	transistor with connect\$3 near3 parallel with connect\$3 near3 series	US-PGPUB; USPAT	OR	ON	2007/01/03 17:18
L4	141	I3 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 15:45
L5	98	I4 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 16:11
L6	1	"20030142046".PN.	US-PGPUB	OR	OFF	2007/01/03 15:33
L7	1	"20030058687".PN.	US-PGPUB	OR	OFF	2007/01/03 15:34
L8	1	"20020047817".PN.	US-PGPUB	OR	OFF	2007/01/03 15:37
L9	1	"6611107".PN.	USPAT; USOCR	OR	OFF	2007/01/03 15:37
L10	1	"6501466".PN.	USPAT; USOCR	OR	OFF	2007/01/03 15:37
L11	420	I3 and "315"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 15:48
L12	386	I11 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 15:45
L13	369	I12 not I5	US-PGPUB; USPAT	OR	ON	2007/01/03 15:46
L14	1	I13 and oled	US-PGPUB; USPAT	OR	ON	2007/01/03 15:47
L15	61	I13 and light adj emitting	US-PGPUB; USPAT	OR	ON	2007/01/03 15:47
L16	0	I3 and "315"/169.1-169.4ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 15:48
L17	27	I3 and "315"/169.1-169.4.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:27
L18	1702	transistor with connect\$3 near3 parallel with connect\$3 near3 series	EPO; JPO; DERWENT	OR	ON	2007/01/03 15:56
L19	68	I18 and light adj emitting	EPO; JPO; DERWENT	OR	ON	2007/01/03 16:07
L20	3	"2003255896"	EPO; JPO; DERWENT	OR	ON	2007/01/03 16:07
L21	3324	transistors with connect\$3 near3 parallel\$2 with connect\$3 near3 (series serially)	US-PGPUB; USPAT	OR	OFF	2007/01/03 17:43

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L22	161	I21 and light adj emitting	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:15
L23	143	I22 not I5	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:11
L24	129	I23 not I15	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:11
L25	102	I24 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 16:16
L26	7681	transistors same connect\$3 near3 parallel\$2 same connect\$3 near3 (series serially)	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:48
L27	535	I26 and light adj emitting	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:15
L28	472	I27 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 16:49
L29	34	I28 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:21
L30	1	"5952789".pn.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:22
L31	1	"6501466".pn.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:22
L32	0	liang-r.xp.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:28
L33	878	liang.xp.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:28
L34	22	I33 and 345/76.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 16:28
L35	1	"20030062524".PN.	US-PGPUB	OR	OFF	2007/01/03 16:28
L36	1	"20010019327".PN.	US-PGPUB	OR	OFF	2007/01/03 16:29
L37	1	"6501466".PN.	USPAT; USOCR	OR	OFF	2007/01/03 16:29
L38	1	"6373454".PN.	USPAT; USOCR	OR	OFF	2007/01/03 16:29
L39	1	"6359605".PN.	USPAT; USOCR	OR	OFF	2007/01/03 16:41
L40	1	"6747418".pn.	USPAT; USOCR	OR	OFF	2007/01/03 16:41
L41	1	"6909242".pn.	USPAT; USOCR	OR	OFF	2007/01/03 16:44
L42	1	"6611107".pn.	USPAT; USOCR	OR	OFF	2007/01/03 16:44
L43	17948	transistors with connect\$3 with parallel\$2	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:48

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L44	26814	transistors with connect\$3 near3 (series serially)	US-PGPUB; USPAT	OR	OFF	2007/01/03 17:05
L45	14881	transistors with connect\$3 near3 parallel\$2	US-PGPUB; USPAT	OR	OFF	2007/01/03 17:05
L46	6787	I44 and I45	US-PGPUB; USPAT	OR	OFF	2007/01/03 16:49
L47	4059	I44 same I45	US-PGPUB; USPAT	OR	OFF	2007/01/03 17:17
L48	117	I47 and display with pixel	US-PGPUB; USPAT	OR	ON	2007/01/03 16:49
L49	71	I48 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:19
L50	7287	transistors with connect\$3 near3 (series serially)	EPO; JPO; DERWENT	OR	OFF	2007/01/03 17:05
L51	4038	transistors with connect\$3 near3 parallel\$2	EPO; JPO; DERWENT	OR	OFF	2007/01/03 17:05
L52	953	I50 and I51	EPO; JPO; DERWENT	OR	OFF	2007/01/03 17:06
L53	2	I52 and display with pixel	EPO; JPO; DERWENT	OR	ON	2007/01/03 17:07
L54	34	I52 and display	EPO; JPO; DERWENT	OR	ON	2007/01/03 17:07
L55	1150	I44 same I45 same (chang\$3 switch\$3)	US-PGPUB; USPAT	OR	OFF	2007/01/03 17:17
L56	3347	transistors with parallel\$2 with (series serially) with (chang\$3 switch\$3)	US-PGPUB; USPAT	OR	ON	2007/01/03 17:41
L57	640	I56 and (display or electronic adj device)	US-PGPUB; USPAT	OR	ON	2007/01/03 17:19
L58	551	I57 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:20
L59	209	I56 and (display adj pixel or electronic adj device)	US-PGPUB; USPAT	OR	ON	2007/01/03 17:20
L60	240	I56 and (display adj3 pixel or electronic adj device)	US-PGPUB; USPAT	OR	ON	2007/01/03 17:40
L61	185	I60 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:22
L62	2705	transistors with parallel\$2 with (series serially) with (chang\$3 switch\$3) with connect\$3	US-PGPUB; USPAT	OR	ON	2007/01/03 17:22
L63	2429	I62 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:40
L64	57	I63 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 17:40

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L65	245	I63 and "315"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 17:27
L66	236	I65 not I64	US-PGPUB; USPAT	OR	ON	2007/01/03 17:27
L67	38	I66 and display	US-PGPUB; USPAT	OR	ON	2007/01/03 17:27
L68	2521	I56 same circuit	US-PGPUB; USPAT	OR	ON	2007/01/03 17:40
L69	2042	I56 with circuit	US-PGPUB; USPAT	OR	ON	2007/01/03 17:40
L70	1834	I69 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:43
L71	49	I70 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/03 17:40
L72	10222	transistors with parallel\$2 with (series serially) with connect\$3	US-PGPUB; USPAT	OR	ON	2007/01/03 17:42
L73	5704	72 with circuit	US-PGPUB; USPAT	OR	ON	2007/01/03 17:42
L74	1597	I21 with circuit	US-PGPUB; USPAT	OR	OFF	2007/01/03 17:43
L75	1418	I74 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:44
L76	412	I21 with circuit with current	US-PGPUB; USPAT	OR	ON	2007/01/03 17:43
L77	827	I21 same circuit same current	US-PGPUB; USPAT	OR	ON	2007/01/03 17:46
L78	726	I77 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/01/03 17:44